

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor

Takanori IWAMATSU Hiroyuki KIYANAGI

Serial No.

09/771,229

Filed

January 26, 2001

Title

CLOCK PHASE DETECTING CIRCUIT AND

CLOCK REGENERATING CIRCUIT EACH

ARRANGED IN RECEIVING UNIT OF MULTIPLEX

RADIO EQUIPMENT

Examiner

Young Toi Tse

Group Art Unit

2634

August 26, 2003

BECEIVED

AUG 2 9 2003

Technology Center 2600

Mail Stop Reissue Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.173(b)

Sir:

Applicant hereby petitions for a three-month extension, a petition pursuant to 37

C.F.R. §1.136(a) and authorization to charge the requisite fee being enclosed.

In response to the Office Action dated February 26, 2003, please amend the

subject application as follows:

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Filed by Express Mail Receipt No. 2 33227568 48

on AUGUST 26, 2003

pursuant to 37 GF.R. 1.10.

Patricia Muir